593 1593 1910	Subclass	ISSUE CLASSIFICATION
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PATENT NUMBER

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SCANNED 20.A. 11	
(m)	

EXAMINER SUBCLASS ART UNIT APPLICATION NO. CONT/PRIOR CLASS 2811 2826 09/970593 F 257

Akira Yoshizawa Tetsu Ishiyama Yoshiyuki Nakamura **APPLICANTS**

TILE

Semilonductor device using interposer substrate and manufacturing method therefor

PTO-2040 12/99

ISSUING CLASSIFICATION					
ORIGI	IAL	CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
			Continued on Issue Slip Inside File Jacket		

			- 1			
TERMINAL	DRAWINGS			CLAIMS ALLOWED		
L_J DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
The term of this patent				NOTICE OF ALI	LOWANCE MAILED	
subsequent to (date) has been disclaimed.	(Assistant Examiner)		(Date)			
The term of this patent shall not extend beyond the expiration date						
of U.S Patent, No.				ISSUE FEE		
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	(Primary I	Examiner)	(Date)			
The terminalmonths of this patent have been disclaimed.				ISSUE BATCH NUMBER		
the patent have been discialined.	(Legal Instrum	ents Examiner)	(Date)			
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